

## PATENT ABSTRACTS OF JAPAN

(11) Publication number: 04356931 A

(43) Date of publication of application: 10 . 12 . 92

(51) Int. CI

H01L 21/336

H01L 29/784

H01L 21/225

H01L 27/12

(21) Application number: 03130908

(71) Applicant:

MATSUSHITA ELECTRIC IND CO

(22) Date of filing: 03 , 06 . 91

(72) Inventor:

FURUTA MAMORU KAWAMURA TETSUYA YOSHIOKA TATSUO

TSUTSU HIROSHI MIYATA YUTAKA

## (54) MANUFACTURE OF THIN FILM TRANSISTOR

## (57) Abstract:

PURPOSE: To provide the manufacturing method, of a thin-film transistor, wherein an organic coating film which contains at least a dopant and an organic solvent is used and a large area can be achieved easily at a low process temperature.

CONSTITUTION: As shown in (a), an amorphous semiconductor film and an insulating film 3 are formed on a light-transmitting substrate 1. Then, as shown in (b), a gate electrode 4 is formed; the insulating film 3 is removed so as to become the shape of the gate electrode 4. Then, as shown in (c), an organic coating film 5 which contains a dopant is formed; after that, the rear side of the substrate is irradiated with an energy beam. Lastly, as shown in (d), the organic coating film 5 is removed selectively; a source electrode and a drain electrode 7 are formed. Thereby, a polycrystalline thin-film transistor is obtained.

COPYRIGHT: (C)1992, JPO& Japio

